

## Öğr. Gör. Dr. Mustafa AKTAN

### Kişisel Bilgiler

E-posta: mustafa.aktan@medipol.edu.tr

Web: <https://avesis.medipol.edu.tr/mustafa.aktan>

### Uluslararası Araştırmacı ID'leri

ScholarID: QCBV4s0AAAAJ

ORCID: 0000-0001-9240-4827

Yoksis Araştırmacı ID: 300185

### Araştırma Alanları

Elektrik-Elektronik Mühendisliği, Elektronik, Elektronik Devreler, Mühendislik ve Teknoloji

### Verdiği Dersler

#### Yüksek Lisans

VLSI Design, Yüksek Lisans, 2019 - 2020

#### Lisans

Linear Algebra and Differential Equations, Lisans, 2019 - 2020

Introduction to Programming for Engineers, Lisans, 2019 - 2020

### SCI, SSCI ve AHCI İndekslerine Giren Dergilerde Yayınlanan Makaleler

- I. **A 70-to-2 v Triboelectric Energy Harvesting System Utilizing Parallel-SSHI Rectifier and DC-DC Converters**  
Kara I., Becermis M., Kamar M. A., Aktan M., Dogan H., Mutlu S.  
IEEE Transactions on Circuits and Systems I: Regular Papers, cilt.68, sa.1, ss.210-223, 2021 (SCI-Expanded)
- II. **An Ultra Low Power Integrated Radio TX Link Supplied from a Switched Capacitor DC-DC Converter in 65-nm CMOS Achieving 2 Mbps**  
Rady R., DOĞAN H., Aktan M., Mohammed S. A., Ozgun M. T.  
IEEE Transactions on Circuits and Systems II: Express Briefs, cilt.67, sa.10, ss.1899-1903, 2020 (SCI-Expanded)
- III. **An alternative carry-save arithmetic for new generation field programmable gate arrays**  
Çİni U., Aktan M., Morgül A.  
Turkish Journal of Electrical Engineering and Computer Sciences, cilt.24, sa.2, ss.435-447, 2016 (SCI-Expanded)

### Diğer Dergilerde Yayınlanan Makaleler

- I. **An algorithm for the design of low-power hardware-efficient FIR filters**  
Aktan M., Yurdakul A., Dündar G.  
IEEE Transactions on Circuits and Systems I: Regular Papers, cilt.55, sa.6, ss.1536-1545, 2008 (Scopus)

## Hakemli Bilimsel Toplantılarda Yayımlanmış Bildiriler

- I. **High performance FIR filter design for 6-input LUT based FPGAs**  
ÇİNİ U., Aktan M.  
IEEE International Conference on Electronics, Circuits, and Systems, ICECS 2015, Cairo, Mısır, 6 - 09 Aralık 2015, cilt.2016-March, ss.653-656, (Tam Metin Bildiri)
- II. **Minimizing Energy by Achieving Optimal Sparseness in Parallel Adders**  
Aktan M., Baran D., Oklobdzija V. G.  
22nd IEEE Symposium on Computer Arithmetic, ARITH 2015, Lyon, Fransa, 22 - 24 Haziran 2015, cilt.2015-August, ss.10-17, (Tam Metin Bildiri)
- III. **Optimal transistor sizing and voltage scaling for minimal energy use at fixed performance**  
Oklobdzija V. G., Aktan M., Baran D.  
7th Argentine School of Micro-Nanoelectronics, Technology and Applications, EAMTA 2012, Cordoba, Arjantin, 4 - 12 Ağustos 2012, ss.1-10, (Tam Metin Bildiri)
- IV. **Circuit topology considerations for energy efficient design of data-driven systems**  
Nawathe V., Wang L., Aktan M., Balsara P., Oklobdzija V. G.  
2012 28th International Conference on Microelectronics, MIEL 2012, Nis, Sırbistan, 13 - 16 Mayıs 2012, ss.415-418, (Tam Metin Bildiri)
- V. **A quick method for energy optimized gate sizing of digital circuits**  
Aktan M., Baran D., Oklobdzija V. G.  
21st International Workshop on Power and Timing Modeling, Optimization, and Simulation, PATMOS 2011, Madrid, İspanya, 26 - 29 Eylül 2011, cilt.6951 LNCS, ss.1-10, (Tam Metin Bildiri)
- VI. **Multiplier structures for low power applications in deep-CMOS**  
Baran D., Aktan M., Oklobdzija V. G.  
2011 IEEE International Symposium of Circuits and Systems, ISCAS 2011, Rio de Janeiro, Brezilya, 15 - 18 Mayıs 2011, ss.1061-1064, (Tam Metin Bildiri)
- VII. **Energy efficient implementation of parallel CMOS multipliers with improved compressors**  
Baran D., Aktan M., Oklobdzija V. G.  
16th ACM/IEEE International Symposium on Low-Power Electronics and Design, ISLPED'10, Austin, TX, Amerika Birleşik Devletleri, 18 - 20 Ağustos 2010, ss.147-152, (Tam Metin Bildiri)
- VIII. **Clocked storage elements robust to process variations**  
Moon J., Aktan M., Oklobdzija V. G.  
2009 8th IEEE International Conference on ASIC, ASICON 2009, Changsha, Çin, 20 - 23 Ekim 2009, ss.827-830, (Tam Metin Bildiri)
- IX. **Switching activity calculation of VLSI adders**  
Baran D., Aktan M., Karimiyan H., Oklobdzija V. G.  
2009 8th IEEE International Conference on ASIC, ASICON 2009, Changsha, Çin, 20 - 23 Ekim 2009, ss.46-49, (Tam Metin Bildiri)
- X. **Exploration of switching activity behavior of addition algorithms**  
Baran D., Aktan M., Karimiyan H., Oklobdzija V. G.  
2009 52nd IEEE International Midwest Symposium on Circuits and Systems, MWSCAS '09, Cancun, Meksika, 2 - 05 Ağustos 2009, ss.523-526, (Tam Metin Bildiri)
- XI. **Design methodology for clocked storage elements robust to process variations**  
Moon J., Aktan M., Oklobdzija V. G.  
2nd International Conference on Advances in Circuits, Electronics and Micro-electronics - CENICS 2009, Sliema, Malta, 11 - 16 Ekim 2009, ss.20-23, (Tam Metin Bildiri)
- XII. **Low-power hardware efficient MMSE equalizer design**  
Aktan M., Dündar G., Koca M.  
2008 4th IEEE International Conference on Circuits and Systems for Communications, ICCSC, Shanghai, Çin, 26 - 28 Mayıs 2008, ss.307-311, (Tam Metin Bildiri)
- XIII. **Design of digital filters for low power applications using integer quadratic programming**

Aktan M., Dündar G.

15th International Workshop on Integrated Circuit and System Design: Power and Timing Modeling, Optimization and Simulation, PATMOS 2005, Leuven, Belçika, 20 - 23 Eylül 2005, cilt.3728 LNCS, ss.137-145, (Tam Metin Bildiri)

- XIV. **Design of digital filters for low power applications by reducing the Hamming distance of the filter coefficients using Mean Field Annealing algorithm MFA Yöntemi ile Katsayıların Hamming Uzaklığını Azaltarak Düşük Güç Uygulamalarına Yönelik Sayısal Süzgeç Tasanımı**

Aktan M., Çini U., Dündar G.

Proceedings of the IEEE 12th Signal Processing and Communications Applications Conference, SIU 2004, Kusadasi, Türkiye, 28 - 30 Nisan 2004, ss.646-648, (Tam Metin Bildiri)

## **Metrikler**

Yayın: 18

Atf (Scopus): 225

H-İndeks (Scopus): 7