

## Doç. Dr. Hakan DOĞAN

### Kişisel Bilgiler

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### Eğitim Bilgileri

Doktora, University of California, Berkeley, Amerika Birleşik Devletleri 2001 - 2005

Lisans, University of Southern California, Electrical Engineering, Amerika Birleşik Devletleri 1995 - 1999

### Yaptığı Tezler

Doktora, Analysis and design of RF CMOS attenuators, University of California, Berkeley, 2005

### Akademik Unvanlar / Görevler

Doç. Dr., İstanbul Medipol Üniversitesi, Mühendislik ve Doğa Bilimleri Fakültesi, Elektrik-Elektronik Mühendisliği Bölümü, 2018 - Devam Ediyor

İstanbul Medipol Üniversitesi, Mühendislik ve Doğa Bilimleri Fakültesi, Elektrik-Elektronik Mühendisliği Bölümü, 2017 - 2018

Yrd. Doç. Dr., İstanbul Şehir Üniversitesi, Mühendislik Ve Doğa Bilimleri Fakültesi, Elektrik-Elektronik Mühendisliği Bölümü, 2013 - 2017

### Akademik İdari Deneyim

İstanbul Şehir Üniversitesi, 2016 - 2017

### Verdiği Dersler

Advanced Integrated Analog Circuit Design, Yüksek Lisans, 2017 - 2018

Elektronik 1, Lisans, 2017 - 2018

Electromekanik Enerji Dönüşümü, Lisans, 2017 - 2018

Microelectronic Circuit Design, Lisans, 2016 - 2017

Communication Circuits, Yüksek Lisans, 2016 - 2017, 2015 - 2016, 2014 - 2015, 2013 - 2014

Analog-Digital Interface Integrated Circuits, Yüksek Lisans, 2016 - 2017

Advanced Integrated Analog Circuit Design, Yüksek Lisans, 2016 - 2017

Digital Logic Design, Lisans, 2015 - 2016, 2014 - 2015, 2013 - 2014

mmWave IC Design, Yüksek Lisans, 2015 - 2016, 2014 - 2015

Control Systems, Lisans, 2015 - 2016, 2014 - 2015, 2013 - 2014

## Yönetilen Tezler

- Hakan D., A 5.84 GHz Integer-n Charge Pump Phase-Locked Loop in 130nm PDSOI CMOS for C-V2X Applications, Yüksek Lisans, T.KAYANSELÇUK(Öğrenci), 2022
- Merve Yüstra D., Hakan D., Design of a digitally controlled ring oscillator for ADPLL, Yüksek Lisans, O.HAMZEH(Öğrenci), 2021
- Hakan D., A high-resolution time to digital converter design for all digital phase-locked loops, Yüksek Lisans, T.EREN(Öğrenci), 2021
- Hakan D., Digital filter and analog LDO design for ADPLL in 180 nm CMOS technology, Yüksek Lisans, Z.NUR(Öğrenci), 2021
- Hakan D., Ultra low power 12-bit 100 ks/s differential sar adc in 65 nm cmos technology, Yüksek Lisans, M.ALİ(Öğrenci), 2020
- Hakan D., A high voltage triboelectric energy harvesting system utilizing parallel-SSHI rectifier and DC-DC converters for sub-5 Hz motions, Doktora, İ.KARA(Öğrenci), 2020
- Hakan D., Logaritmik Güç Detektörü Tasarımı, Yüksek Lisans, C.AYAN(Öğrenci), 2019
- Hakan D., Nano-structured triboelectric nano generators for internet-of-things (IOT) applications, Yüksek Lisans, K.ÜNLÜ(Öğrenci), 2018
- Hakan D., Ultra low power, low noise, and fully integrated receiver for short range wireless communications, Yüksek Lisans, H.EL(Öğrenci), 2017
- Hakan D., Ultra-low power, low-voltage transmitter at ism band for short range transceivers, Yüksek Lisans, R.RADY(Öğrenci), 2017
- Hakan D., DC-DC regulators for ultra low power applications, Yüksek Lisans, S.ELHOSAINY(Öğrenci), 2017
- Hakan D., High gain, high bandwidth, wide icmr, and highly linear fully differential amplifier with large dynamic range and process corner configurable output stage, Yüksek Lisans, A.MOZAMMEL(Öğrenci), 2017
- Hakan D., A wide-current range switched capacitor DC-DC converter utilizing frequency, interleaving and switch-size scaling techniques, Yüksek Lisans, S.MOHAMMED(Öğrenci), 2016
- Hakan D., Low phase noise frequency synthesizer, Yüksek Lisans, I.F.(Öğrenci), 2016

## SCI, SSCI ve AHCI İndekslerine Giren Dergilerde Yayınlanan Makaleler

- I. **An Ultra Low Power Integrated Radio TX Link Supplied from a Switched Capacitor DC-DC Converter in 65-nm CMOS Achieving 2 Mbps**  
Rady R., DOĞAN H., Aktan M., Mohammed S. A., Ozgun M. T.  
IEEE Transactions on Circuits and Systems II: Express Briefs, cilt.67, sa.10, ss.1899-1903, 2020 (SCI-Expanded)
- II. **A fully integrated 2.4 dB NF capacitive cross coupling CG-LNA for LTE band**  
Abdelhamid A. A., Ozgun M. T., DOĞAN H.  
Analog Integrated Circuits and Signal Processing, cilt.99, sa.1, ss.159-166, 2019 (SCI-Expanded)
- III. **A low power receiver front-end design with tunable notch filter for TX leakage and blocker suppression**  
Ozgun M. T., Abdelhamid A., DOĞAN H.  
IEEE Transactions on Circuits and Systems I: Regular Papers, cilt.66, sa.3, ss.1180-1191, 2019 (SCI-Expanded)
- IV. **A novel biasing technique for low phase noise voltage controlled oscillators**  
Albittar I. F., DOĞAN H., Ozgun M. T.  
Microelectronics Journal, cilt.72, ss.120-125, 2018 (SCI-Expanded)

## Diğer Dergilerde Yayınlanan Makaleler

- I. **A COMPACT GAN POWER AMPLIFIER MODULE FOR NEW GENERATION CELLULAR BASESTATIONS**  
TÜRK B. B., HÜR CAN F., SAVCI H. S., Dogan H.  
Black Sea Journal of Engineering and Science, cilt.7, sa.3, ss.587-593, 2024 (Hakemli Dergi)
- II. **A highly linear wide-band tunable LNA for military radio applications**  
DOĞAN H.  
Istanbul University - Journal of Electrical and Electronics Engineering, cilt.18, sa.1, ss.19-25, 2018 (Scopus)

## **Hakemli Kongre / Sempozyum Bildiri Kitaplarında Yer Alan Yayınlar**

- I. **A GaN-based Power Amplifier Module Design for 5G Base Stations**  
Türk B. B., Hür can F., SAVCI H. Ş., DOĞAN H.  
International Conference on Cyber Security and Computer Science, Karabük, Türkiye, 29 Mart 2023
- II. **6 GHz Low Noise Amplifier design with 65nm CMOS for 5G/6G Applications 5G/6G Uygulamaları için 65nm CMOS ile 6GHz Düşük Gürültülü Yükselteç Tasarımı**  
Eren T., Oktay Z. N., DOĞAN H., SAVCI H. Ş.  
12th International Conference on Electrical and Electronics Engineering, ELECO 2020, Bursa, Türkiye, 26 - 28 Kasım 2020, ss.88-92
- III. **A Fully Integrated Low Power LNA in 65nm CMOS Technology Suitable for ZigBee, Low-Power WiFi and Bluetooth Low Energy**  
DOĞAN H.  
8th International Advanced Technologies Symposium, Elazığ, Türkiye, 19 - 22 Ekim 2017, cilt.2, ss.404-409
- IV. **A Fully Integrated Ultra-Low Power LNA in 65nm Cmos Technology Suitable For Sensor Network Applications**  
DOĞAN H.  
3rd International Conference on Engineering and Natural Sciences, Budapest, Macaristan, 3 - 07 Mayıs 2017
- V. **A highly integrated wideband LNA with multiple inputs for multi-band mobile devices**  
Abdelhamid A. A., Ozgun M. T., DOĞAN H.  
2016 IEEE 59th International Midwest Symposium on Circuits and Systems (MWSCAS), Abu Dhabi, United Arab Emirates, 16 - 19 Ekim 2016
- VI. **A 65nm dual-band 3-stream 802.11n MIMO WLAN SoC**  
Abdollahi-Alibeik S., Weber D., DOĞAN H., Si W. W., Baytekin B., Komijani A., Chang R., Vakili-Amini B., Lee M., Gan H., et al.  
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- VII. **A Single-Chip CMOS Radio SoC for v2.1 Bluetooth Applications**  
Weber D., Si W. W., Abdollahi-Alibeik S., Lee M., Chang R., DOĞAN H., Luschas S., Husted P.  
2008 IEEE International Solid-State Circuits Conference - Digest of Technical Papers, San-Francisco, Kostarika, 3 - 07 Şubat 2008, ss.364-365
- VIII. **A dc-2.5GHz wide dynamic-range attenuator in 0.13µm CMOS technology**  
DOĞAN H., Meyer R. G., Niknejad A. M.  
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- IX. **A DC-10GHz linear-in-dB attenuator in 0.13 µm CMOS technology**  
DOĞAN H., Meyer R. G., Niknejad A. M.  
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